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09998848 - GAI: 2191

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		09998848
	Filing Date		2001-11-15
	First Named Inventor	Kenneth Y. Ogami	
	Art Unit	2191	
	Examiner Name	VO, TED T	
	Attorney Docket Number	CD01177	

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6825689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
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	8	7127630	B1	2006-10-24	Snyder	Entire Document

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /T.V./

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	10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
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	20	6981090	B1	2005-12-27	Kutz et al.	Entire Document
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#### U.S.PATENT APPLICATION PUBLICATIONS

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	1	20020121679	A1	2002-10-29	Bazarjani et al.	Entire Document

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#### FOREIGN PATENT DOCUMENTS

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	29	<del>USPTO Final Rejection for 10/002,217 (CD01207) dated 02/06/08; 10 pages</del>	<input type="checkbox"/>
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	<del>36</del>	<del>USPTO Advisory Action for 10/001,477 (CD01203) dated 10/10/08; 3 pages</del>	<input type="checkbox"/>
	37	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance;" Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
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	40	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al., filed on 09/26/2001; 25 pages	<input type="checkbox"/>
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